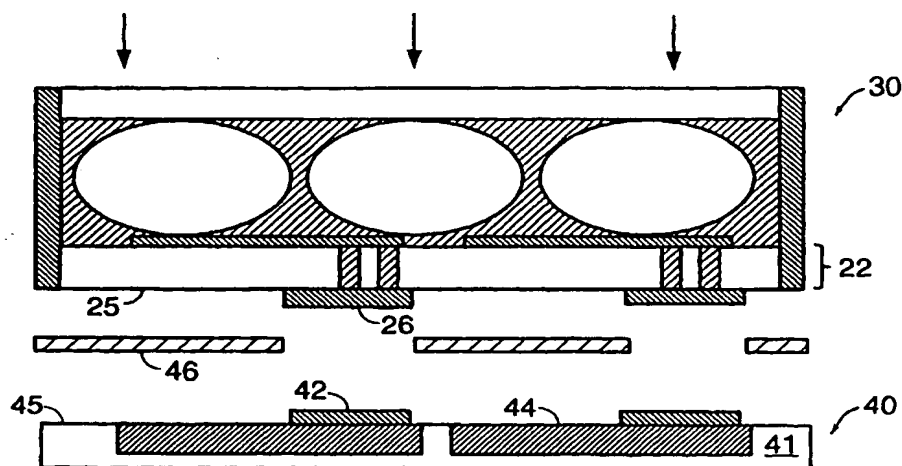


## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification <sup>7</sup> : <b>G02F 1/167, 1/1334</b>		<b>A1</b>	(11) International Publication Number: <b>WO 00/36465</b>
		(43) International Publication Date: 22 June 2000 (22.06.00)	
(21) International Application Number: PCT/US99/29922		(81) Designated States: AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).	
(22) International Filing Date: 15 December 1999 (15.12.99)			
(30) Priority Data: 60/112,330 15 December 1998 (15.12.98) US 09/289,036 9 April 1999 (09.04.99) US 09/338,412 22 June 1999 (22.06.99) US			
(71) Applicant: E INK CORPORATION [US/US]; 45 Spinelli Place, Cambridge, MA 02138 (US).			
(72) Inventors: DUTHALER, Gregg, M.; Apartment 6E, 1243 Beacon Street, Brookline, MA 02446 (US). KAZLAS, Peter, T.; 447 Washington Street, Brookline, MA 02446 (US). DRZAIC, Paul; 2 Paddock Lane, Lexington, MA 02421 (US).			
(74) Agent: LANZA, John, D.; Testa, Hurwitz & Thibault, LLP, High Street Tower, 125 High Street, Boston, MA 02110 (US).		<b>Published</b> <i>With international search report.</i> <i>Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i>	

(54) Title: ASSEMBLY OF MICROENCAPSULATED ELECTRONIC DISPLAY



## (57) Abstract

An electro-optical device is manufactured by separately manufacturing an electronic component and an optical component and subsequently integrating the electronic and the optical components. Separate fabrication of the two components allows each component to be manufactured using processes which optimize its properties.

**FOR THE PURPOSES OF INFORMATION ONLY**

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Larvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KE	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LI	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		
EE	Estonia						

## ASSEMBLY OF MICROENCAPSULATED ELECTRONIC DISPLAYS

### Related Applications

The present application claims priority to provisional application U.S.S.N. 60/112,330 filed on December 15, 1998, the entire disclosure of which is incorporated herein by reference. The present application is a continuation-in-part of utility applications U.S.S.N. 09/338,412 filed on June 22, 1999 and U.S.S.N. 09/289,036 filed on April 9, 1999, the entire disclosures of which  
5 are incorporated herein by reference.

### Field of the Invention

This invention generally relates to methods of manufacturing an electronic display, and more particularly, to methods of assembling electronic displays.

### Background of the Invention

10 An electronic display includes optical components such as liquid crystals and electrophoretic particles and electronic components such as electrodes and driving circuitry. The optical components and the electronic components have differing performance criteria. For example, it is desirable for the optical components to optimize reflectivity, contrast ratio and response time, while it is desirable for the electronic components to optimize conductivity,  
15 voltage-current relationship, and capacitance, or to possess memory, logic, or other higher-order electronic device capabilities. Therefore, a process for manufacturing an optical component may not be ideal for manufacturing an electronic component, and vice versa. For example, a process for manufacturing an electronic component can involve processing under high temperatures. The processing temperature can be in the range from about 300°C to about 600°C. Subjecting many

optical components to such high temperatures, however, can be harmful to the optical components by degrading the elements (i.e., the electrophoretic particles or liquid crystals) chemically or by causing mechanical damage.

### Summary of the Invention

5           One way to circumvent this problem is to change the chronology of display fabrication, in which the electronic components requiring high temperature processing are processed first, and the optical components requiring low temperature processing are processed second. Another way to circumvent this problem is to process the optical components and the electrical components separately and then integrate the two components, afterwards.

10           The present invention relates to a method of manufacturing an electro-optical device. In one aspect, the electro-optical device is manufactured in accordance with the following steps. First, a modulating layer, a pixel layer, and a circuit layer are provided. The modulating layer includes a first substrate and an electro-optical material provided adjacent the first substrate. The modulating layer is capable of changing a visual state upon application of an electric field. The  
15           pixel layer comprises a second substrate. A plurality of pixel electrodes are provided on a front surface of the second substrate and a plurality of contact pads are provided on a rear surface of the second substrate. Each pixel electrode is connected to a contact pad through a via extending through the second substrate. The circuit layer includes a third substrate and at least one circuit element. The modulating layer, the pixel layer, and the circuit layer are integrated to form the  
20           electro-optical device.

          In one embodiment, the pixel layer and the modulating layer are integrated first to form a subassembly, and thereafter, the circuit layer and the subassembly are integrated to form the electro-optical device. For example, the pixel layer and the modulating layer can be integrated

through encapsulation. Alternatively, an edge of the pixel layer and an edge of the modulating layer can be sealed together. The circuit layer and the subassembly can be integrated by bonding the two layers. For example, the circuit layer and the subassembly can be bonded by inserting an adhesive layer comprising an anisotropically conductive material between the two layers.

5           In another embodiment, the pixel layer and the circuit layer are integrated first to form a subassembly and thereafter, the modulating layer and the subassembly are integrated to form the electro-optical device.

          In one embodiment, the electro-optical material comprises a plurality of capsules, each capsule comprising a plurality of particles dispersed in a fluid. For example, the plurality of  
10       particles can be electrophoretic particles. In another embodiment, the electro-optical material comprises liquid crystals. In still another embodiment, the electro-optical material comprises a plurality of capsules, each capsule comprising a bichromal sphere dispersed in a fluid.

          In one embodiment, the modulating layer comprises a flexible substrate. For example, the modulating layer can be an organic substrate.

15           In one embodiment, the pixel layer comprises an insulative substrate. The pixel layer can be formed by printing or evaporating a conductive material on the front surface of the substrate to form the pixel electrodes.

          In one embodiment, the circuit layer includes one or more of the following elements: a data line driver, a select line driver, a power supply, a sensor, a logic element, a memory device  
20       and a communication device. For example, the circuit layer can include non-linear devices such as transistors. The transistors can be made by printing organic-based field effect transistors on a front surface of the circuit layer.

In one embodiment, the modulating layer, the pixel layer, and the circuit layer are tested prior to being integrated.

In another aspect, the invention features an electro-optical device manufactured using the methods described herein. The electro-optical device can be an electronic display.

5

### Brief Description of the Drawings

The foregoing and other objects, features and advantages of the present invention, as well as the invention itself, will be more fully understood from the following description of preferred embodiments, when read together with the accompanying drawings, in which:

Figure 1 shows a cross-section view of a modulating layer of an electronic display,  
10 according to one embodiment of the present invention.

Figure 2 shows a cross-section view of a pixel layer of an electronic display, according to one embodiment of the present invention.

Figure 3 shows a cross-section view of a subassembly of the modulating layer of Figure 1 and the pixel layer of Figure 2, according to one embodiment of the present invention.

15 Figure 4a illustrates integration of the subassembly of Figure 3 and a circuit layer, according to one embodiment of the present invention.

Figure 4b illustrates integration of the subassembly of Figure 3 and a circuit layer, according to another embodiment of the present invention.

20 Figure 5 shows a cross-section of an electronic ink assembly, according to one embodiment of the present invention.

Figure 6a illustrates integration of the electronic ink assembly of Figure 5 and a second circuit layer, according to one embodiment of the present invention.

Figure 6b shows a cross-section of an electronic ink integrated using the method illustrated in Figure 6a.

Figure 7a illustrates integration of a pixel layer and a circuit layer to form a subassembly, according to one embodiment of the present invention.

5        Figure 7b illustrates integration of a subassembly of a pixel layer and a circuit layer to a modulator layer, according to one embodiment of the present invention.

Figure 8a shows a partial cross-section view of an electronic display media, according to one embodiment of the present invention.

10       Figure 8b shows a partial cross-section view of an electronic display media, according to one embodiment of the present invention.

Figure 8c shows a partial cross-section view of an electronic display media, according to one embodiment of the present invention.

Figure 8d shows a partial cross-section view of an electronic display media, according to one embodiment of the present invention.

15       Figure 9a shows a first surface of a pixel layer, according to one embodiment of the present invention.

Figure 9b shows a second surface of a pixel layer, according to one embodiment of the present invention.

20       Figure 10 shows a partial cross-section view of transistors for addressing an electronic display, according to one embodiment of the present invention.

Figure 11 illustrates a connection between a transistor and electrodes of an electronic display, according to one embodiment of the present invention.

Detailed Description of Preferred Embodiments

The present invention relates to a method of assembling or packing an electronic display. Referring to Figure 1, a modulating layer 10 prepared using methods to be described, which optimize the optical qualities of the modulating layer 10, is provided. The modulating layer 10 includes a substrate 12 and a display media 14 provided next to the substrate 12. The substrate 12 includes a front common electrode 16 deposited on a first surface 13 of the substrate 12 next to the display media 14. The display media 14 includes microcapsules 18 dispersed in a binder 20. Each microcapsule 18 includes an electro-optical material. An electro-optical material refers to a material which displays an optical property in response to an electrical signal. Examples of electro-optical material can be electrophoretic particles or a liquid crystalline fluid dispersed in a solvent. An electro-optical material can also be bichromal spheres dispersed in a solvent. Details of electro-optical materials within the microcapsules 18 will be discussed further in reference to Figures 8a-8d. An important property of the electro-optical material within the microcapsules 18 is that the material is capable of displaying one visual state upon application of an electric field and a different visual state upon application of a different electric field.

Referring to Figure 2, a pixel layer 22, prepared using methods to be discussed which optimize the electrical properties of the pixel layer 22, is provided. The pixel layer 22 includes a substrate 23, pixel electrodes 24 provided on a first surface 21 of the substrate 23, and contact pads 26 provided on a second surface 25 of the substrate 23. Each pixel electrode 24 is electrically connected to a contact pad 26 through a via 28. In order to maximize probability of electrical contact between the pixel electrode 24 and the contact pad 26, more than one via 28 can be provided between each pixel electrode 24 and its corresponding contact pad 26, as illustrated in Figure 2. The pixel layer 22 will be discussed in detail with reference to Figures 9a and 9b.



The modulating layer 10 of Figure 1 and the pixel layer 22 of Figure 2, prepared separately are now integrated as illustrated in Figure 3 to form a subassembly 30. The pixel electrodes 24 are brought in contact with a second surface 11 of the modulating layer 10 such that the common electrode 12 is provided next to the first surface 13 of the display media 14 and the pixel electrodes 24 are provided next to the second surface 11 of the display media 14. The pixel layer 22 can be joined with the modulating layer 10 by providing an adhesive material between the pixel layer 22 and the modulating layer 10. The adhesive material has electrical, mechanical, and chemical properties that are compatible with those of the pixel layer 22 and the modulating layer 10. To ensure intimate connection between the pixel layer 22 and the modulating layer 10, the subassembly 30 may be pressed together using standard pressing equipment well known to those skilled in the art, such as vacuum laminators and thermal presses.

The edges of the subassembly 30 can be sealed using a seal 32 as shown in Figure 3. The material for the edge seal 32 can be selected from a number of commercially available materials, such as one or two-part epoxies. In another embodiment, the subassembly 30 is encapsulated in a protective material. The material for the encapsulant can be selected from a number of commercially available transparent materials, such as vapor-deposited parylene. In this embodiment, the contact pads 26 remain exposed to provide electrical connection with a circuit layer. Alternatively, encapsulation with a protective material can take place after a final assembly of an electronic display is provided. The protective material used for encapsulation protects the electro-optic material and the electronic elements from the environment.

Referring to Figures 4a and 4b, the subassembly 30 is integrated with the circuit layer 40. The circuit layer 40 includes a substrate 41, pixel electrode contacts 42 provided on a first surface 45 of the substrate 41 and pixel circuitry and logic 44 provided on the substrate 41. Details of the circuit layer 40 will be discussed in reference to Figures 10 and 11. In one

embodiment, the subassembly 30 is bonded to the circuit layer 40 using bonding techniques known to those skilled in the art, such as thermocompression, thermosonic bonding or mechanical bonding.

In the embodiment of Figure 4a, an adhesive layer 46 is provided between the second  
5 surface 25 of the pixel layer 22 of the subassembly 30 and a first surface 45 of the circuit layer 40. To improve adhesion and planarization, the adhesive layer 46 can be provided on the second surface 25 of the pixel layer 22 around the contact pads 26 and the first surface 45 of the circuit layer 30 around the pixel electrode contacts 42. The adhesive layer 46, for example, can be made of a film coated on both sides with an adhesive material. Examples of such adhesive layers  
10 include Double Coated Film Tape Nos. 9443, 443, and 444 available from 3M Company (St. Paul, MN). In one embodiment, the adhesive layer 46 is printed on the second surface 25 of the pixel layer 22 and/or the first surface 45 of the circuit layer 40.

In the embodiment of Figure 4b, an anisotropic conductive film 47 is provided between the second surface 25 of the pixel layer 22 and the first surface 45 of the circuit layer 40. An  
15 anisotropic conductive film 47 is conductive through one axis, such as the z-axis only. The anisotropic conductive film 47 can consist of silver particles dispersed in an adhesive matrix such as Z-axis Adhesive Film Nos. 5303 and 7303 available from 3M Company (St. Paul, MN). The anisotropic conductive film 47 can provide the electrical feedthrough between the contact pads 26 and the pixel electrode contacts 42.

20 After providing the adhesive layer 46, 47 between the pixel layer 22 and the circuit layer 30, a pressure is applied to the subassembly 30 and the circuit layer 40 to bond them together. Standard equipment known to those skilled in the art, such as vacuum laminators and thermal presses, can be used for assembling the subassembly 30 and the circuit layer 40 to form the

assembly 50, as shown in Figure 5. In one embodiment, the pixel layer 22 and the circuit layer 40 are connected through edge connectors in addition to the vias 28.

Referring to Figures 6a and 6b, the circuit layer 40 of the assembly 50 can be further connected to a second circuit layer 70. The second circuit layer 70 can include additional electronics for driving the electronic display. The second circuit layer 70 can include a flexible printed circuit board 72 and high performance integrated circuits 74 that perform control functions for the electronic display. The first circuit layer 40 and the second circuit layer 70 can be connected through a ribbon cable 78. The second circuit layer 70 can be used in addressing the display media 14. Alternatively, the second circuit layer 70 can be used in addressing a second display media provided next to the second circuit layer 70. In this embodiment, an electronic display includes two display surfaces.

The order of assembling the modulating layer 10, the pixel layer 22 and the circuit layer 40 is not limited to the order described in reference to Figures 1-3, 4a-4b, and 5. Referring to Figures 7a and 7b, the pixel layer 22 can be first laminated to the circuit layer 40, forming a subassembly 35, and subsequently the modulating layer 10 can be laminated to the subassembly 35. This order of assembling the modulating layer 10, the pixel layer 22 and the circuit layer 40 is desirable where the bonding pressure and temperature required to laminate the pixel layer 22 to the circuit layer 40 is outside the processing regime of the modulating layer 10.

The alignment of the modulating layer 10, the pixel layer 22 and the circuit layer 40 can be facilitated by mechanical and/or photographic alignment markers. For example, the alignment markers can be printed on the second surface 25 of the pixel layer 22 and the first surface 45 of the circuit layer 40 in precise locations, preferably at opposite corners on the pixel layers 22 and the circuit layer 40, and the relative positions of the pixel layer 22 and the circuit

layer 44 layers can be adjusted until all markers are aligned. For a plastic or deformable substrate, an alignment feature can be embossed on one substrate and punched through the other substrate to provide an interlocking alignment joint.

The method of assembling the electronic display according to the present invention, allows each of the modulating layer, the pixel layer and the circuit layer to be processed individually to optimize its performance characteristics. In addition, each of the modulating layer, the pixel layer, and the circuit layer can be tested after processing prior to assembly. Such capability reduces cost of production. In one embodiment, a subassembly of the modulating layer and the pixel layer can be tested by simply contacting the subassembly with the circuit layer, prior to assembly.

In one embodiment, the display media used in forming the electronic display comprises a particle-based display media. In one detailed embodiment, the particle-based display media comprises an electronic ink. An electronic ink is an optoelectronically active material which comprises at least two phases: an electrophoretic contrast media phase and a coating/binding phase. The electrophoretic phase comprises, in some embodiments, a single species of electrophoretic particles dispersed in a clear or dyed medium, or more than one species of electrophoretic particles having distinct physical and electrical characteristics dispersed in a clear or dyed medium. In some embodiments the electrophoretic phase is encapsulated, that is, there is a capsule wall phase between the two phases. The coating/binding phase includes, in one embodiment, a polymer matrix that surrounds the electrophoretic phase. In this embodiment, the polymer in the polymeric binder is capable of being dried, crosslinked, or otherwise cured as in traditional inks, and therefore a printing process can be used to deposit the electronic ink onto a substrate.

The optical quality of an electronic ink is quite distinct from other electronic display materials. The most notable difference is that the electronic ink provides a high degree of both reflectance and contrast because it is pigment based (as are ordinary printing inks). The light scattered from the electronic ink comes from a very thin layer of pigment close to the top of the viewing surface. In this respect it resembles an ordinary, printed image. Also, electronic ink is easily viewed from a wide range of viewing angles in the same manner as a printed page, and such ink approximates a Lambertian contrast curve more closely than any other electronic display material. Since electronic ink can be printed, it can be included on the same surface with any other printed material, including traditional inks. Electronic ink can be made optically stable in all display configurations, that is, the ink can be set to a persistent optical state. Fabrication of a display by printing an electronic ink is particularly useful in low power applications because of this stability.

Electronic ink displays are novel in that they can be addressed by DC voltages and draw very little current. As such, the conductive leads and electrodes used to deliver the voltage to electronic ink displays can be of relatively high resistivity. The ability to use resistive conductors substantially widens the number and type of materials that can be used as conductors in electronic ink displays. In particular, the use of costly vacuum-sputtered indium tin oxide (ITO) conductors, a standard material in liquid crystal devices, is not required. Aside from cost savings, the replacement of ITO with other materials can provide benefits in appearance, processing capabilities (printed conductors), flexibility, and durability. Additionally, the printed electrodes are in contact only with a solid binder, not with a fluid layer (like liquid crystals). This means that some conductive materials, which would otherwise dissolve or be degraded by contact with liquid crystals, can be used in an electronic ink application. These include opaque metallic inks for the rear electrode (e.g., silver and graphite inks), as well as conductive

transparent inks for either substrate. These conductive coatings include conducting or semiconducting colloids, examples of which are indium tin oxide and antimony-doped tin oxide. Organic conductors (polymeric conductors and molecular organic conductors) also may be used. Polymers include, but are not limited to, polyaniline and derivatives, polythiophene and derivatives, poly3,4-ethylenedioxythiophene (PEDOT) and derivatives, polypyrrole and derivatives, and polyphenylenevinylene (PPV) and derivatives. Organic molecular conductors include, but are not limited to, derivatives of naphthalene, phthalocyanine, and pentacene. Polymer layers can be made thinner and more transparent than with traditional displays because conductivity requirements are not as stringent.

10 Figure 8a shows an electrophoretic display 130. The binder 132 includes at least one capsule 134, which is filled with a plurality of particles 136 and a dyed suspending fluid 138. In one embodiment, the particles 136 are titania particles. When a direct-current electric field of the appropriate polarity is applied across the capsule 134, the particles 136 move to the viewed surface of the display and scatter light. When the applied electric field is reversed, the particles  
15 136 move to the rear surface of the display and the viewed surface of the display then appears dark.

Figure 8b shows another electrophoretic display 140. This display comprises a first set of particles 142 and a second set of particles 144 in a capsule 141. The first set of particles 142 and the second set of particles 144 have contrasting optical properties. For example, the first set of  
20 particles 142 and the second set of particles 144 can have differing electrophoretic mobilities. In addition, the first set of particles 142 and the second set of particles 144 can have contrasting colors. For example, the first set of particles 142 can be white, while the second set of particles 144 can be black. The capsule 141 further includes a substantially clear fluid. The capsule 141

has electrodes 146 and 146' disposed adjacent it. The electrodes 146, 146' are connected to a source of voltage 148, which may provide an alternating-current (AC) field or a direct-current (DC) field to the capsule 441. Upon application of an electric field across the electrodes 146, 146', the first set of particles 142 move toward electrode 146', while the second set of particles  
5 144 move toward electrode 146.

Figure 8c shows a suspended particle display 150. The suspended particle display 150 includes needle-like particles 152 in a transparent fluid 154. The particles 152 change their orientation upon application of an AC field across the electrodes 156, 156'. When the AC field is applied, the particles 152 are oriented perpendicular with respect to the display surface and the  
10 display appears transparent. When the AC field is removed, the particles 152 are randomly oriented and the display 150 appears opaque.

The electrophoretic displays provided in Figures 8a-8c are exemplary only, and other electrophoretic displays can be used in accordance with the present invention. Other examples of electrophoretic displays are described in commonly owned, copending U.S. Patent Application  
15 Serial Numbers 08/935,800 and 09/140,792 which are incorporated herein by reference.

The successful construction of an encapsulated electrophoretic display requires the proper interaction of a binder for binding the capsules to a substrate, electrophoretic particles, fluid (for example, to surround the electrophoretic particles and provide a medium for migration), and a capsule membrane (for example, for enclosing the electrophoretic particles and fluid). These  
20 components must all be chemically compatible. The capsule membranes may engage in useful surface interactions with the electrophoretic particles, or may act as an inert physical boundary between the fluid and the binder. Polymer binders may set as adhesives between capsule membranes and electrode surfaces.

Various materials may be used to create electrophoretic displays. Selection of these materials is based on the functional constituents of the display to be manufactured. Such functional constituents include, but are not limited to, particles, dyes, suspending fluids, stabilizing/charging additives, and binders. In one embodiment, types of particles that may be used to fabricate suspended particle displays include scattering pigments, absorbing pigments and luminescent particles. Such particles may also be transparent. Exemplary particles include titania, which may be coated in one or two layers with a metal oxide, such as aluminum oxide or silicon oxide, for example. Such particles may be constructed as corner cubes. Luminescent particles may include, for example, zinc sulfide particles. The zinc sulfide particles may also be encapsulated with an insulative coating to reduce electrical conduction. Light-blocking or absorbing particles may include, for example, dyes or pigments. Types of dyes for use in electrophoretic displays are commonly known in the art. Useful dyes are typically soluble in the suspending fluid, and may further be part of a polymeric chain. Dyes may be polymerized by thermal, photochemical, and chemical diffusion processes. Single dyes or mixtures of dyes may also be used.

A suspending (*i.e.*, electrophoretic) fluid may be a high resistivity fluid. The suspending fluid may be a single fluid, or it may be a mixture of two or more fluids. The suspending fluid, whether a single fluid or a mixture of fluids, may have its density substantially matched to that of the particles within the capsule. The suspending fluid may be halogenated hydrocarbon, such as tetrachloroethylene, for example. The halogenated hydrocarbon may also be a low molecular weight polymer. One such low molecular weight polymer is poly(chlorotrifluoroethylene). The degree of polymerization for this polymer may be from about 2 to about 10.



Furthermore, capsules may be formed in, or later dispersed in, a binder. Materials for use as binders include water-soluble polymers, water-dispersed polymers, oil-soluble polymers, thermoset polymers, thermoplastic polymers, and UV- or radiation-cured polymers.

While the examples described here are listed using encapsulated electrophoretic displays, there are other particle-based display media that also should work well, including encapsulated suspended particles and rotating ball displays. Other display media, such as liquid crystals and magnetic particles, also can be useful.

In some cases, a separate encapsulation step of the process is not necessary. The electrophoretic fluid may be directly dispersed or emulsified into the binder (or a precursor to the binder material) to form what may be called a "polymer-dispersed electrophoretic display." In such displays, the individual electrophoretic phases may be referred to as capsules or microcapsules even though no capsule membrane is present. Such polymer-dispersed electrophoretic displays are considered to be subsets of encapsulated electrophoretic displays.

In an encapsulated electrophoretic display, the binder material surrounds the capsules and separates the two bounding electrodes. This binder material must be compatible with the capsule and bounding electrodes and must possess properties that allow for facile printing or coating. It may also possess barrier properties for water, oxygen, ultraviolet light, the electrophoretic fluid, or other materials. Further, it may contain surfactants and cross-linking agents, which could aid in coating or durability. The polymer-dispersed electrophoretic display may be of the emulsion or phase separation type.

In another detailed embodiment, the display media can comprise a plurality of bichromal spheres shown in Figure 8d. A bichromal sphere 160 typically comprises a positively charged hemisphere 162 of a first color and a negatively charged hemisphere 164 of a second color in a

liquid medium 166. Upon application of an electric field across the sphere 160 through a pair of electrodes 168, 168', the sphere 160 rotates and displays the color of one of the two hemispheres 162, 164.

In one embodiment, an electronic display is created by printing the entire display or a  
5 portion of the display. The term "printing" is intended to include all forms of printing and coating, including: ink-jet printing, premetered coating such as patch die coating, slot or extrusion coating, slide or cascade coating, and curtain coating; roll coating such as knife over roll coating, forward and reverse roll coating; gravure coating; dip coating; spray coating; meniscus coating; spin coating; brush coating; air knife coating; screen printing processes;  
10 electrostatic printing processes; thermal printing processes; and other similar techniques.

Referring to Figures 9a and 9b, the pixel layer 200 is described in more detail. The pixel layer 200 includes a substrate 202 having a first surface 204 and a second surface 206. The substrate 202, for example, can be made of an insulating polymeric material such as polyethylene terephthalate (PET, polyester), polyethersulphone (PES), polyimide film (e.g. Kapton, available  
15 from Dupont [Wilmington, DE]; Upilex, available from Ube Corporation [Japan]), or polycarbonate. An advantage of the insulator substrate 202 is that the substrate 202 protects the display media, as well as the pixel electrodes 208 from the environment. An array of pixel electrodes 208 are provided on the first surface 204 of the substrate 202. The pixel electrodes 208 are arranged in a manner to obtain high aperture ratio or fill factor. An electrically  
20 conductive material can be evaporated or printed on the first surface 204 of the substrate 202 to form the pixel electrodes 208. The pixel electrodes 208 are connected to the second surface 206 of the substrate 202 through electrical vias 210. The electrical vias 210 can be formed using one of several techniques. For example, holes can be made in the pixel layer 200 by either laser drilling, etching or pricking the holes. The holes are then filled by printing a low resistivity

slurry such as carbon, graphite, or silver particles in a polymeric compound. An array of contact pads 212 are provided on the second surface 206 of the pixel layer 200. The contact pads 212 can be made using one of many available methods known to those skilled in the art. For example, a conductive material can be evaporated or printed on the second surface 206 of the substrate 202 to form the contact pads 212.

The pixel layer can be further processed to include various electrical, thermal and optical layers to improve display performance. For example, thermoelectric (TE) heaters or coolers can be provided on either surface of the substrate 202, as permitted by space availability, to ensure that the electro-optical material stays within its thermal operating regime. Thermoelectric heaters may be integrated by providing traces of resistive material on a surface of the substrate 202. By passing an electrical current through these traces, the display temperature can be regulated. Conversely, thermoelectric coolers, such as a Peltier cooler, can be used to move thermal energy from the display to a suitable heat sink.

The pixel layer can also include grounded thin metal foils and/or reflective or opaque light shields. Grounded thin-metal foils or light shields can be used to enhance the optical performance of the display and optically shield photosensitive microelectronics from light. For example, the pixel layer or the circuit layer may incorporate semiconducting materials, many of which are known to be photosensitive. Thin foils or light shields may be incorporated onto one or more surfaces of the pixel layer or the circuit layer so that the photosensitive materials are optically shielded from all incident light.

The circuit layer can include electrodes such as column electrodes and row electrodes, non-linear devices, and logic or driver circuitry for addressing the display. For example, the circuit layer can include transistors shown in Figures 10 and 11.

Referring to Figure 10, an organic-based field effect transistor 210 includes a substrate 212, a gate electrode 214 provided on the substrate 212, a dielectric layer 216 provided on the gate electrode 214, an organic semiconductor 218 provided on the dielectric layer 216, and a source electrode 220 and a drain electrode 222 provided on the organic semiconductor 218. The transistor 210 is electrically connected to a pixel electrode 200, a column electrode 204 and a row electrode 206. The pixel electrode 200 is connected to the drain electrode of the transistor 210. The column electrode 204 is connected to the source electrode of the transistor 210.

The substrate 212 can be flexible. For example, the substrate 212 can be made of a polymer, such as polyethylene terephthalate (PET, polyester), polyethersulphone (PES), polyimide film (Kapton, Upilex), or polycarbonate. Alternatively, the substrate 212 can be made of an insulator such as undoped silicon, glass, or other plastic. The substrate 212 can also be patterned to serve as an electrode. Alternatively, the substrate 212 can be a metal foil insulated from the gate electrode 214 by a non-conducting material. The gate electrode 214, the source electrode 220, and the drain electrode 222, for example, can comprise a metal such as gold. Alternatively, the electrodes 214, 220, 222 can comprise a conductive polymer such as polythiophene or polyaniline, a printed conductor such as a polymer film comprising metal particles such as silver or nickel, a printed conductor comprising a polymer film containing graphite or some other conductive carbon material, or a conductive oxide such as tin oxide or indium tin oxide, or metal electrodes such as aluminum or gold.

The dielectric layer 216, for example, can comprise a silicon dioxide layer. Alternatively, the dielectric layer 216 can comprise an insulating polymer such as polyimide and its derivatives or poly-vinyl phenol, polymethylmethacrylate, polyvinylidenedifluoride, an inorganic oxide such as metal oxide, an inorganic nitride such as silicon nitride, or an inorganic/organic composite material such as an organic-substituted silicon oxide, or a sol-gel organosilicon glass. The

dielectric layer 216 can also comprise a benzocyclobutene (BCB) derivative marketed by Dow Chemical (Midland, MI), spin-on glass, or dispersions of dielectric colloid materials in a binder or solvent.

The semiconductor layer 218 can be an organic polymer. In one embodiment, the organic  
5 semiconductor comprises a polymeric or oligomeric semiconductor. Examples of suitable  
polymeric semiconductors include, but are not limited to, polythiophene, poly(3-alkyl), alkyl-  
substituted oligothiophene, polythienylenevinylene, poly(para-phenylenevinylene) and doped  
versions of these polymers. An example of suitable oligomeric semiconductor is alpha-  
hexathienylene. Horowitz, *Organic Field-Effect Transistors*, Adv. Mater., 10, No. 5, p. 365  
10 (1998) describes the use of unsubstituted and alkyl-substituted oligothiophenes in transistors. A  
field effect transistor made with regioregular poly(3-hexylthiophene) as the semiconductor layer  
is described in Bao et al., *Soluble and Processable Regioregular Poly(3-hexylthiophene) for Thin  
Film Field-Effect Transistor Applications with High Mobility*, Appl. Phys. Lett. 69 (26), p. 4108  
(December 1996). A field effect transistor made with  $\alpha$ -hexathienylene is described in U.S.  
15 Patent No. 5,659,181.

In another embodiment, the organic semiconductor 218 comprises a carbon based  
compound. Examples of suitable carbon based compounds include, but are not limited to,  
pentacene, phthalocyanine, benzodithiophene, fullerene, buckminsterfullerene,  
tetracyanonaphthoquinone, and tetrakisimethylanimoethylene. The materials provided above for  
20 forming the substrate, the dielectric layer, the electrodes, or the semiconductor layer are  
exemplary only. Other suitable materials known to those skilled in the art having properties  
similar to those described above can be used in accordance with the present invention.

The field effect transistor configuration provided in Figures 10 and 11 are exemplary only. Other transistor designs known to those skilled in the art can be used in accordance with the present invention. For example, a top gate structure in which the source and drain electrodes are placed adjacent the substrate, covered by the dielectric layer, which in turn is covered by the semiconductor and gate electrode, can also be used in accordance with the present invention.

According to the present invention, electrodes, non-linear devices such as the transistors of Figures 10 and 11, and logic and driving circuitry can be manufactured using any appropriate fabrication process known to those skilled in the art including, but not limited to, deposition, evaporation, lithography, printing, and coating. For example, an entire transistor that is organic-based can be printed in its entirety as described in co-pending commonly owned U.S. patent application serial no. 09/289,036, incorporated herein by reference. In another example, a transistor can be fabricated on a first substrate and subsequently removed and provided on a substrate of the circuit layer as described in co-pending commonly owned U.S. patent application serial no. 09/338,412, incorporated herein by reference.

While the invention has been particularly shown and described with reference to specific preferred embodiments, it should be understood by those skilled in the art that various changes in form and detail may be made therein without departing from the spirit and scope of the invention as defined by the appended claims.

What is claimed is:

Claims

1. A method of manufacturing an electro-optical device comprising:
  - a) providing a modulating layer comprising a first substrate and an electro-optical material provided adjacent the first substrate, the modulating layer being capable of changing a visual state upon application of an electric field;
  - b) providing a pixel layer comprising a second substrate, a plurality of pixel electrodes provided on a front surface of the second substrate and a plurality of contact pads provided on a rear surface of the second substrate, each pixel electrode connected to a contact pad through a via extending through the second substrate;
  - c) providing a circuit layer comprising a third substrate and at least one circuit element; and
  - d) integrating the modulating layer, the pixel layer and the circuit layer to form the electro-optical device.
2. The method of claim 1 wherein step d) comprises d1) integrating the pixel layer and the modulating layer to form a subassembly and d2) integrating the circuit layer and the subassembly, thereby forming the electro-optical device.
3. The method of claim 1 wherein the electro-optical material comprises a plurality of capsules, each capsule comprising a plurality of particles dispersed in a fluid.
4. The method of claim 3 wherein the plurality of particles comprises electrophoretic particles.
5. The method of claim 1 wherein the electro-optical material comprises liquid crystals.

- 1 6. The method of claim 1 wherein the electro-optical material comprises a plurality of  
2 capsules, each capsule comprising a bichromal sphere dispersed in a fluid.
- 1 7. The method of claim 1 wherein the modulating layer comprises a flexible first substrate.
- 1 8. The method of claim 1 wherein the modulating layer comprises an organic first substrate.
- 1 9. The method of claim 1 wherein the modulating layer comprises a first substrate and a  
2 transparent common electrode provided on the first substrate.
- 1 10. The method of claim 1 wherein the pixel layer comprises a second insulative substrate.
- 1 11. The method of claim 1 further comprising forming the pixel layer by printing a  
2 conductive material on the front surface of the second substrate to form the pixel electrodes.
- 1 12. The method of claim 1 further comprising forming the pixel layer by evaporating a  
2 conductive material on the front surface of the second substrate to form the pixel electrodes.
- 1 13. The method of claim 1 further comprising forming the pixel layer by providing a plurality  
2 via holes through the second substrate and filling the via holes with a conductive material.
- 1 14. The method of claim 2 wherein step d1) comprises encapsulating the pixel layer and the  
2 modulating layer.
- 1 15. The method of claim 2 wherein step d1) comprises sealing together an edge of the pixel  
2 layer and an edge of the modulating layer.
- 1 16. The method of claim 2 wherein step d1) comprises integrating the pixel layer and the  
2 modulating layer by providing the front surface of the pixel layer adjacent the electro-optical  
3 material of the modulating layer.



- 1 17. The method of claim 2 wherein step d2) comprises bonding the circuit substrate layer and  
2 the subassembly.
- 1 18. The method of claim 2 wherein step d1) comprises inserting an adhesive layer between  
2 the pixel layer and modulating layer.
- 1 19. The method of claim 2 wherein step d2) comprises bonding the circuit layer to the rear  
2 surface of the pixel layer.
- 1 20. The method of claim 19 wherein step d2) comprises bonding the circuit layer and the  
2 subassembly through one of thermocompression, thermosonic bonding and mechanical bonding.
- 1 21. The method of claim 19 wherein step d2) comprises bonding the circuit layer and the  
2 subassembly by inserting an adhesive between the circuit layer and the subassembly.
- 1 22. The method of claim 21 wherein step d2) comprises inserting an adhesive layer  
2 comprising an anisotropically conductive material between the circuit layer and the subassembly.
- 1 23. The method of claim 2 further comprising printing an adhesive layer adjacent the rear  
2 surface of the pixel layer around the contact pads prior to performing step d2).
- 1 24. The method of claim 1 wherein the circuit layer comprises at least one of a data line  
2 driver, a select line driver, a power supply, a sensor, a logic element, a memory device, and a  
3 communication device.
- 1 25. The method of claim 2 wherein step a) comprises providing a first modulating layer and a  
2 second modulating layer, step b) comprises providing a first pixel layer and a second pixel layer,  
3 step d1) comprises integrating the first pixel layer and the first modulating layer to form a first  
4 assembly and integrating the second pixel layer and the second modulating layer to form a

5 second assembly, and step d2) comprises placing the circuit layer between the first assembly and  
6 the second assembly.

1 26. The method of claim 1 further comprising testing the modulating layer and the circuit  
2 layer prior to performing step d).

1 27. The method of claim 1 wherein the circuit layer comprises a plurality of non-linear  
2 devices.

1 28. The method of claim 1 wherein the circuit layer comprises a plurality of organic-based  
2 field effect transistors.

1 29. The method of claim 1 further comprising printing an organic-based field effect transistor  
2 array on a front surface of the third substrate of the circuit layer.

1 30. The method of claim 2 wherein the circuit layer comprises a plurality of contacts  
2 provided on a front surface of the third substrate and step d2) comprises integrating the  
3 modulating layer and the subassembly such that a contact of the circuit layer is provided adjacent  
4 the contact pad of the pixel layer.

1 31. The method of claim 1 wherein step d) comprises d1) integrating the pixel layer and the  
2 circuit layer to form a subassembly and d2) integrating the modulating layer and the subassembly  
3 to form the electro-optical device.

1 32. An electro-optical device manufactured by the method of claim 1.

1 33. An electro-optical device manufactured by the method of claim 2.

1 34. An electro-optical device manufactured by the method of claim 31.

1/8

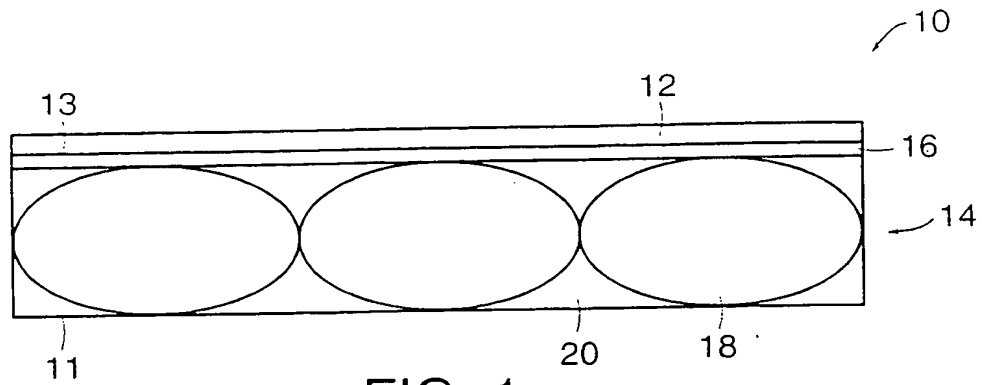


FIG. 1

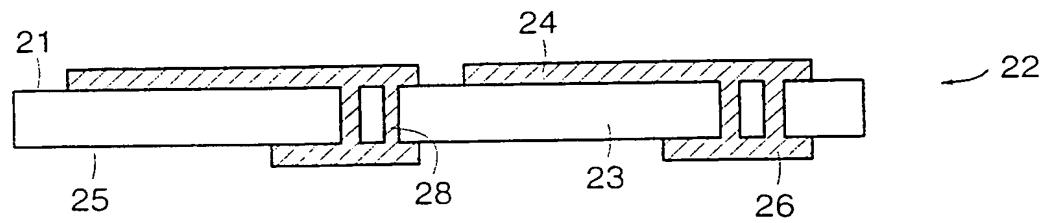


FIG. 2

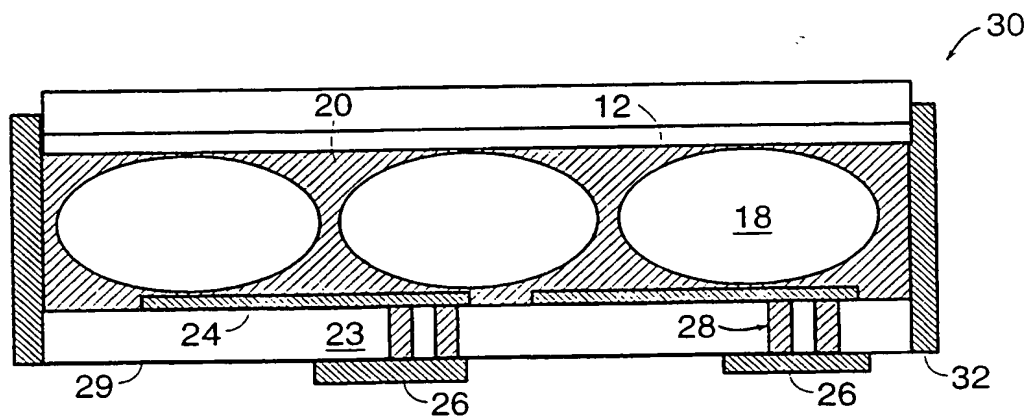


FIG. 3

2/8

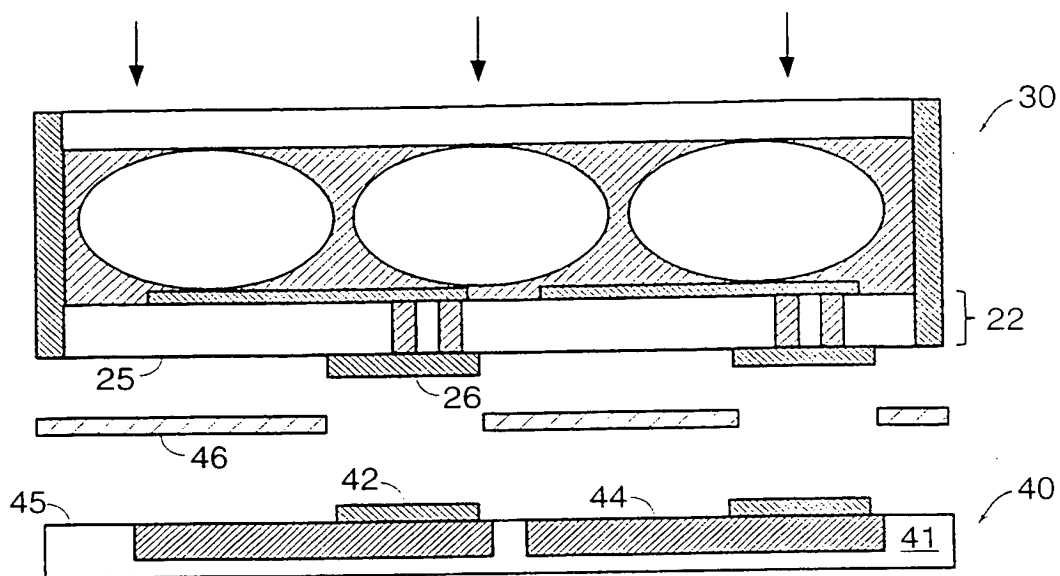


FIG. 4a

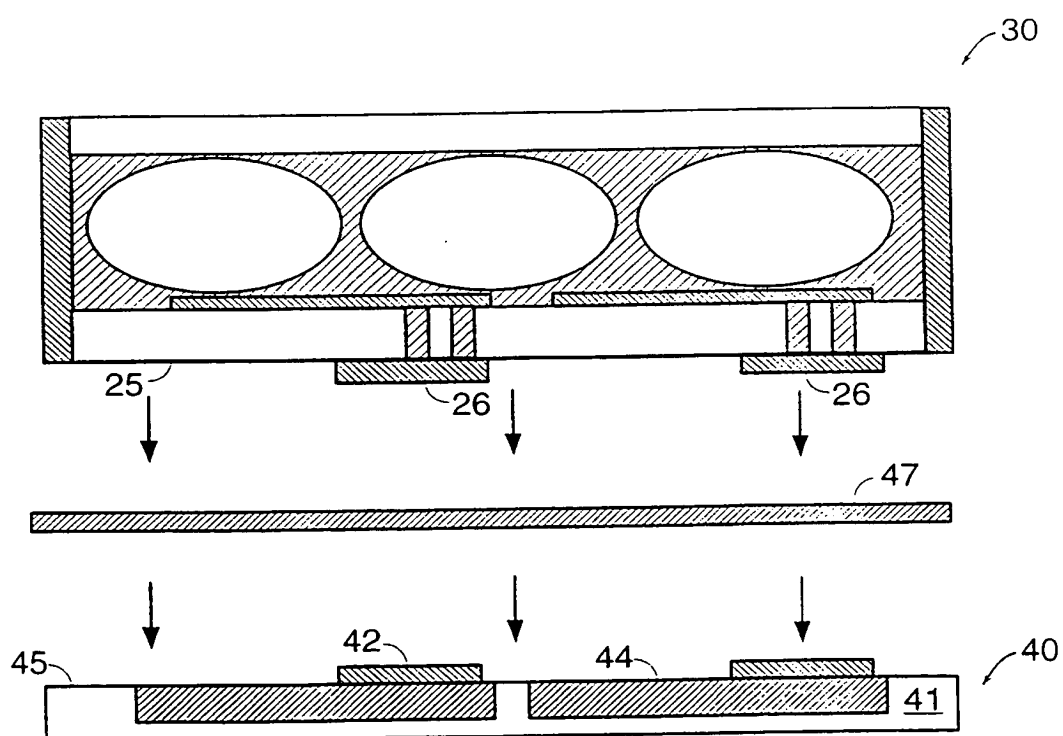


FIG. 4b

3/8

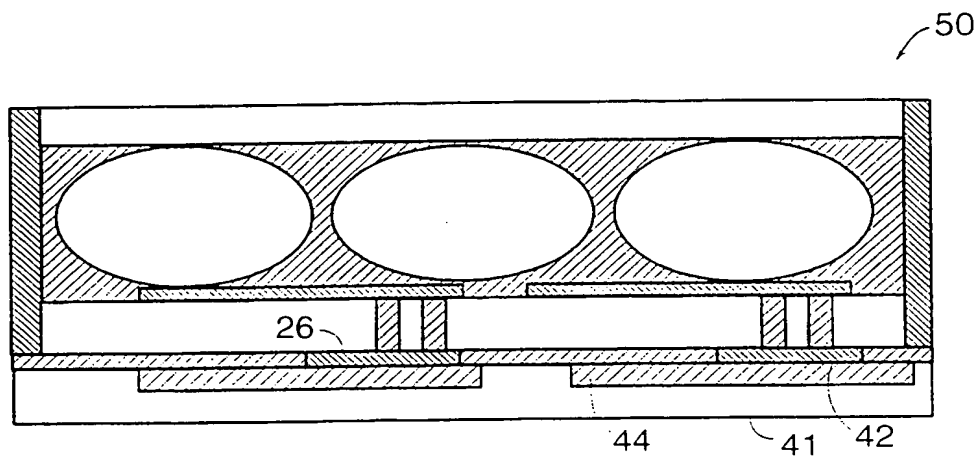


FIG. 5

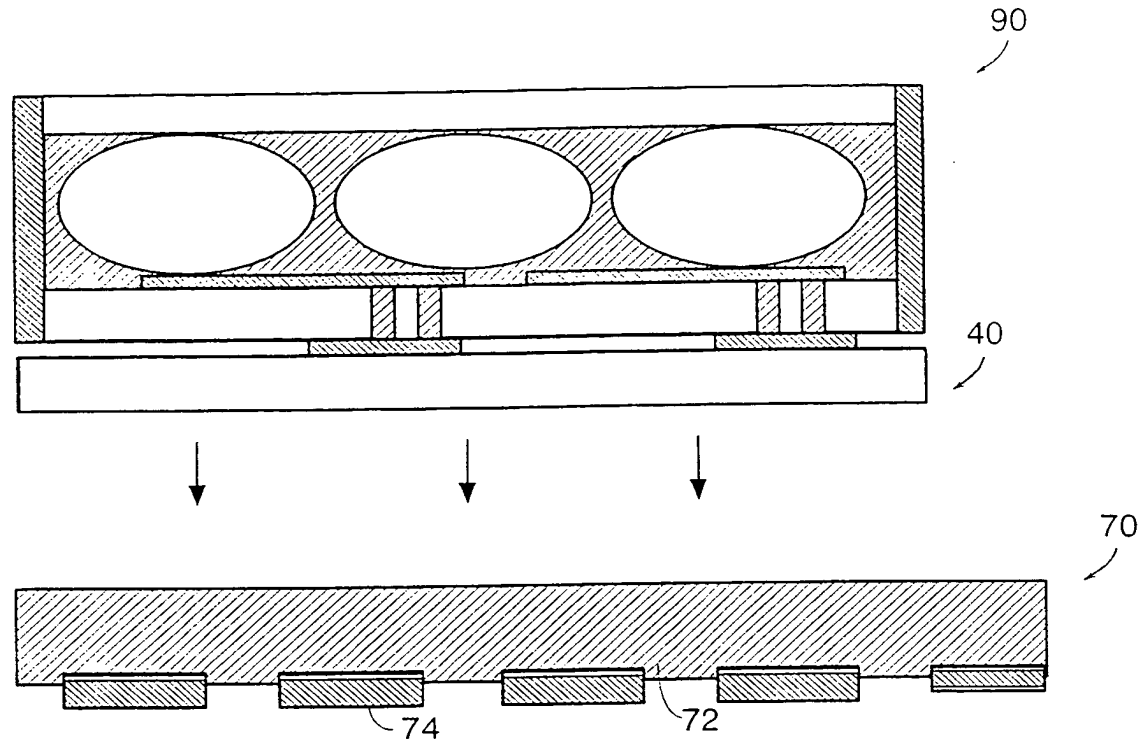


FIG. 6a

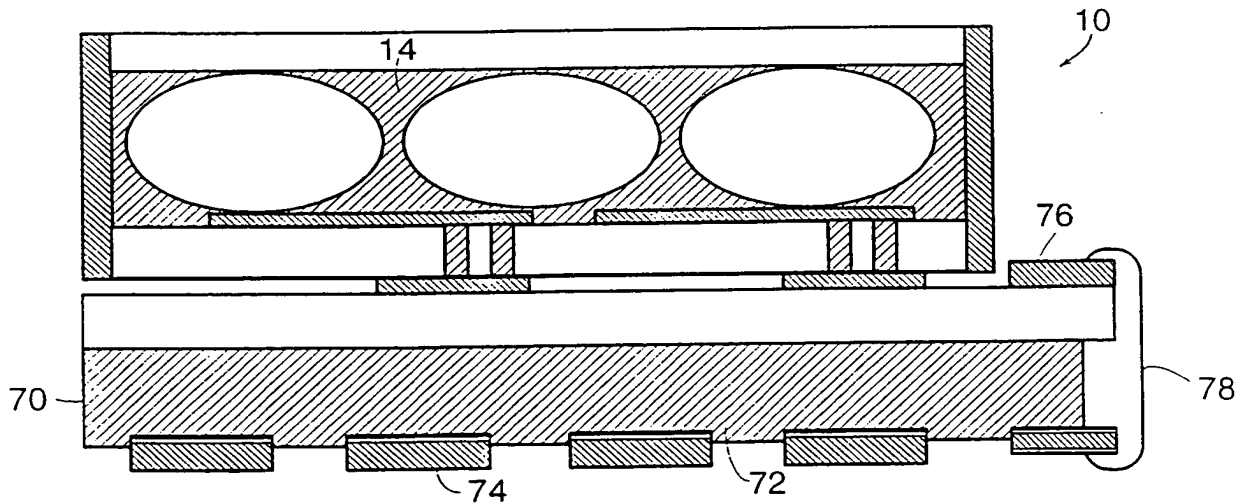


FIG. 6b

5/8

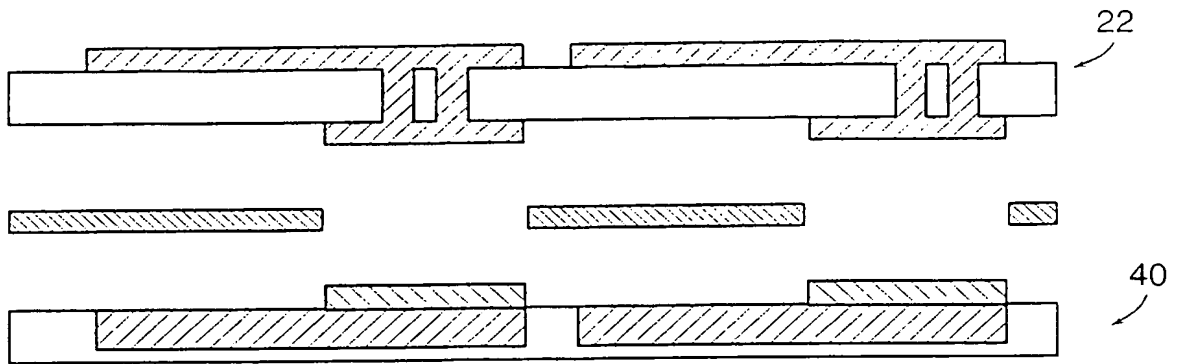


FIG. 7a

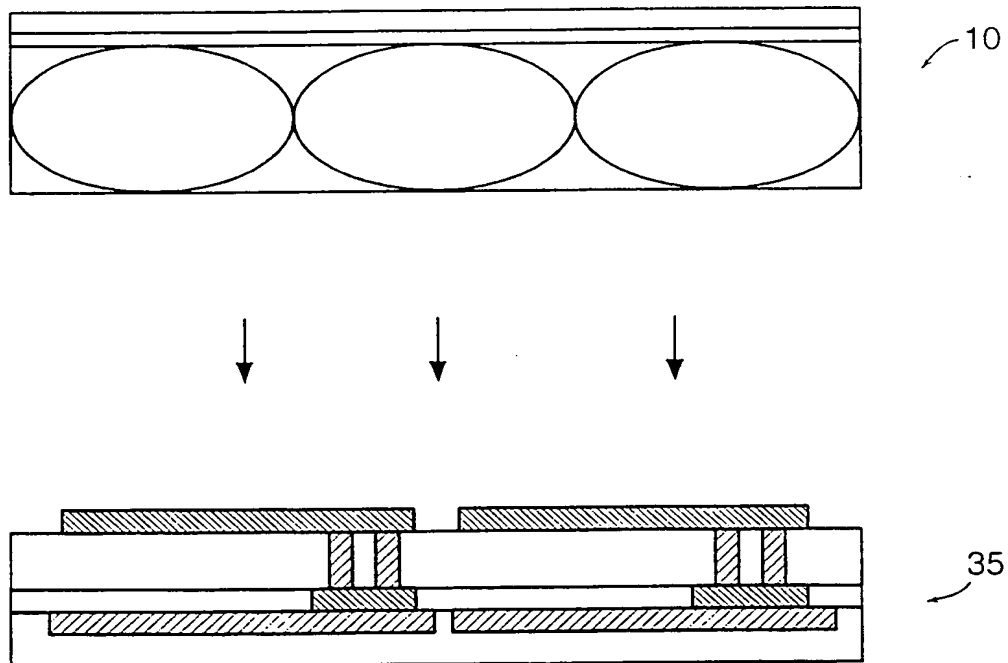
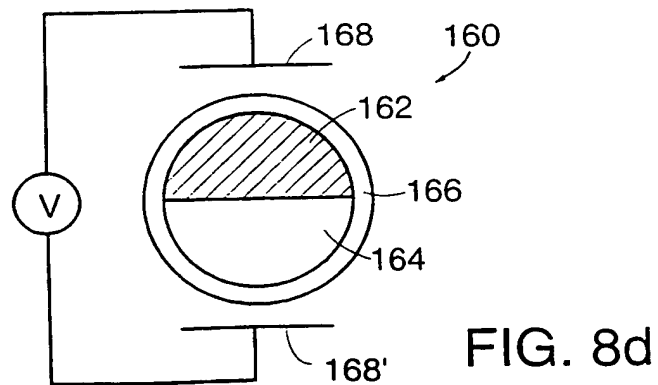
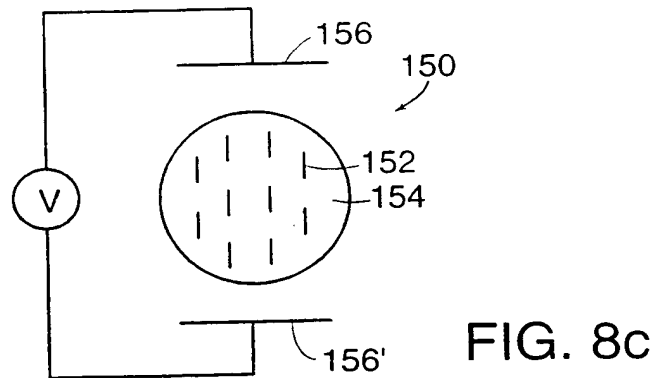
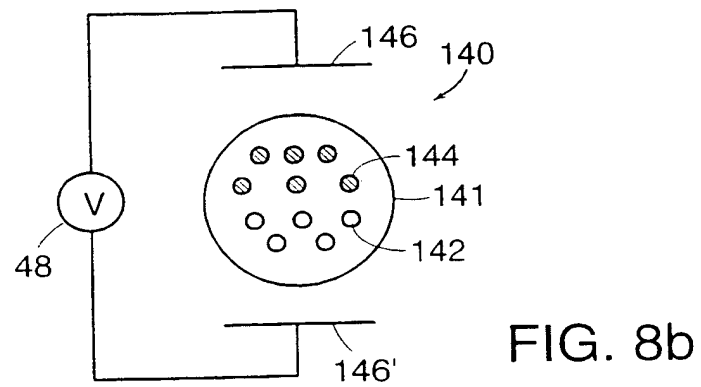
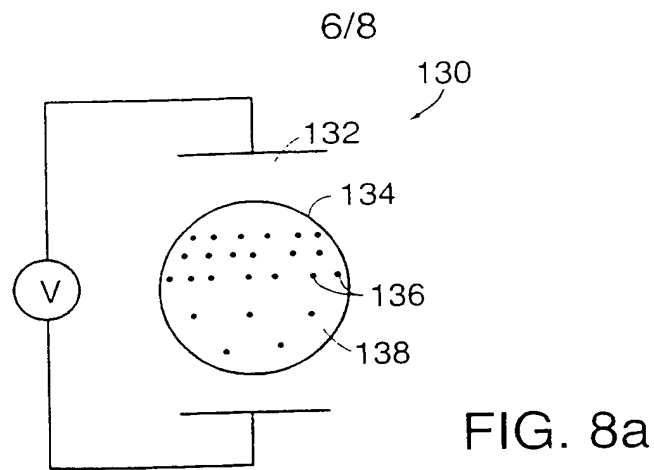


FIG. 7b





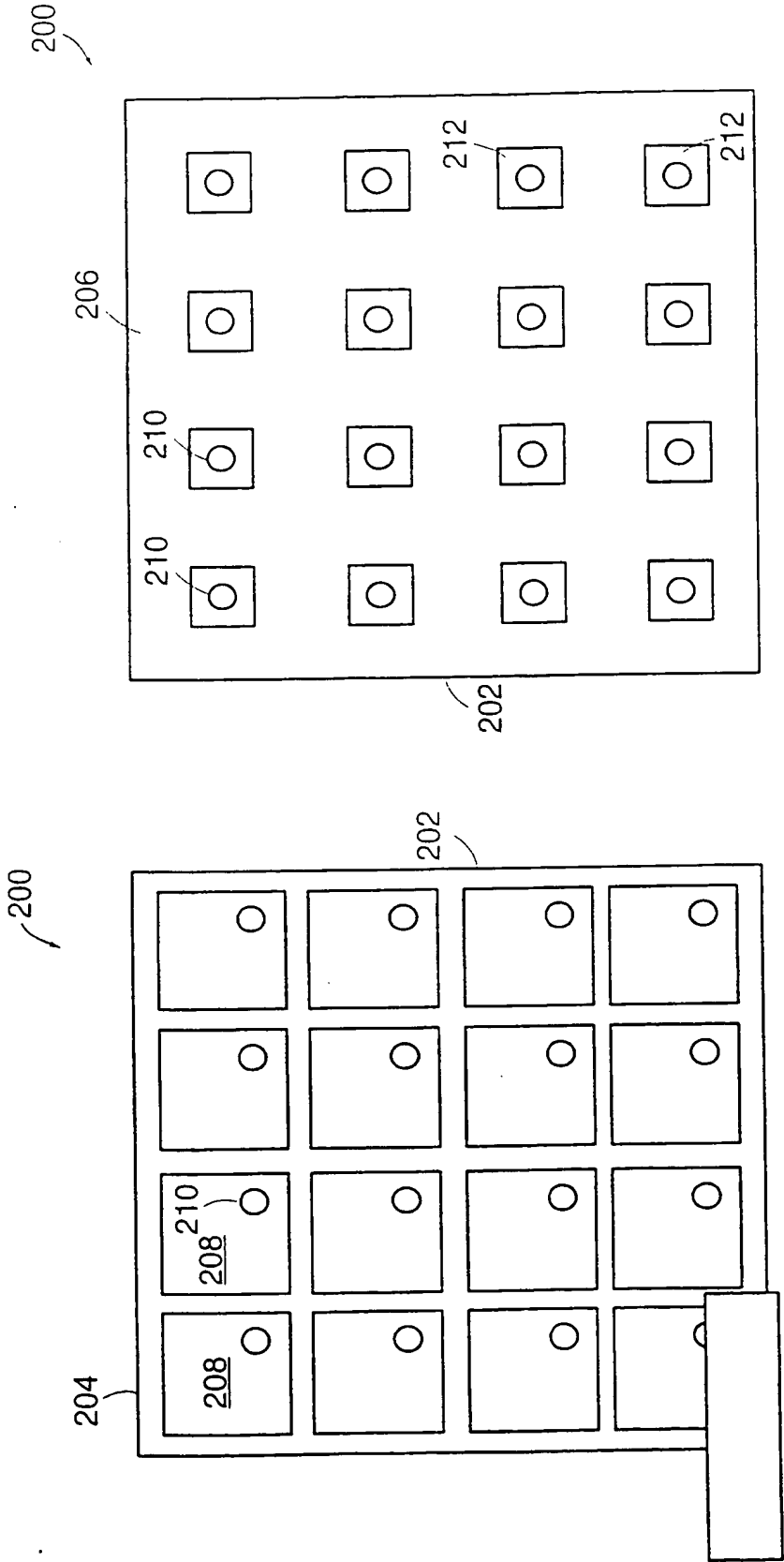


FIG. 9b

FIG. 9A

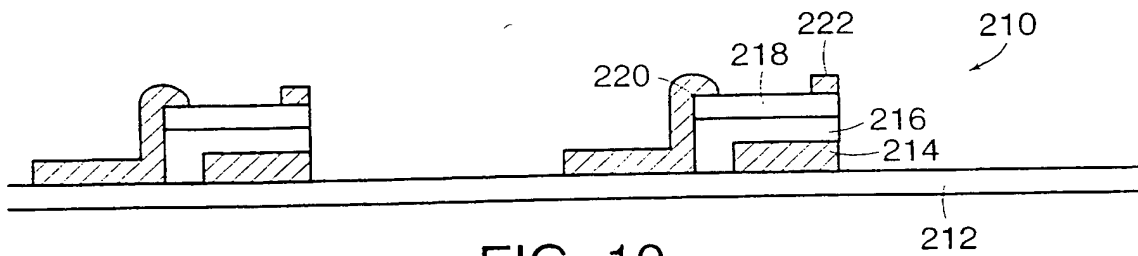


FIG. 10

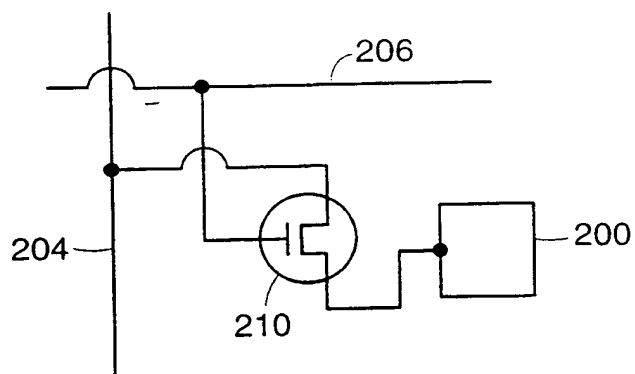


FIG. 11

## INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 99/29922

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 G02F1/167 G02F1/1334

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G02F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	NAKAMURA E ET AL: "Development of electrophoretic display using microcapsulated suspension" SID INTERNATIONAL SYMPOSIUM DIGEST OF TECHNICAL PAPERS, US, SANTA ANA, CA: SID, vol. 29, 17 May 1998 (1998-05-17), pages 1014-1017, XP002114866 ISSN: 0098-966X column 5 -column 6; figure 6	1-4, 6-13, 16-26, 28-34
X	DRZAIC P ET AL: "44.31: A PRINTED AND ROLLABLE BISTABLE ELECTRONIC DISPLAY" SID INTERNATIONAL SYMPOSIUM DIGEST OF TECHNICAL PAPERS, US, SANTA ANA, CA: SID, vol. 29, 17 May 1998 (1998-05-17), XP002127322 ISSN: 0098-966X column 5, paragraph 4; figure 3	1-4, 6-13, 16-26, 28-34



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

## \* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"Z" document member of the same patent family

Date of the actual completion of the international search

5 April 2000

Date of mailing of the international search report

12/04/2000

Name and mailing address of the ISA

European Patent Office, P.B. 6818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040, Tx. 31 851 epo nl,  
Fax: (+31-70) 340-3018

Authorized officer

G111, R

## INTERNATIONAL SEARCH REPORT

International Application No.

PCT/US 99/29922

## C. (Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	TOYAMA J ET AL: "AN ELECTROPHORETIC MATRIX DISPLAY WITH EXTERNAL LOGIC AND DRIVER DIRECTLY ASSEMBLED TO THE PANEL" SID INTERNATIONAL SYMPOSIUM DIGEST OF TECHNICAL PAPERS, US, SANTA ANA, SID, vol. 25, 14 June 1994 (1994-06-14), pages 588-590, XP000462732 figures 1,3	1,24
A	JACKSON T N ET AL: "ORGANIC THIN-FILM TRANSISTORS FOR ORGANIC LIGHT-EMITTING FLAT-PANEL DISPLAY BACKPLANES" IEEE JOURNAL OF SELECTED TOPICS IN QUANTUM ELECTRONICS, US, IEEE SERVICE CENTER, vol. 4, no. 1, 1 January 1998 (1998-01-01), pages 100-104, XP000766109 ISSN: 1077-260X abstract	28,29